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[K Pettis](#)

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Profile guided code positioning - group of 3 »

K Pettis, RC Hansen - Proceedings of the ACM SIGPLAN 1990 conference on ..., 1990 - portal.acm.org

... 1. Introduction Traditional **optimization** techniques attempt to improve pro ... page faults for virtual **memory** machines ... the support for basic block **profiling** and the ...

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Optimization of instruction fetch mechanisms for high issue rates - group of 4

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TM Conte, KN Menezes, PM Mills, BA Patel - Computer Architecture, 1995. Proceedings. 22nd Annual ..., 1995 - ieeexplore.ieee.org

... the six instruction per cycle **IBM POWER2** architecture ... The effect of com- piler **optimization** is analyzed to ... assumes that the instruction **memory** bandwidth into ...

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End-user tools for application performance analysis using hardware counters - group of 7 »

K London, J Dongarra, S Moore, P Mucci, K Seymour, ... - International Conference on Parallel and Distributed ..., 2001 - cs.utk.edu

... SvPablo, being developed at the **IBM Advanced Computing** ... point instructions as the **profiling** metric. ... on single processor and shared **memory optimization** techniques ...

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Memory behavior of the SPEC2000 benchmark suite - group of 3 »

S Sair, M Charney - **IBM TJ Watson Research Center Technical Report**, 2000 - cs.ucsd.edu ... ssair@cs.ucsd.edu mark.charney@us.ibm.com ... language compiler mcf C 190 Combinatorial

optimization crafty C 2.1 ... computing the **memory** component of the cycles per ...

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Dynamic and transparent binary translation - group of 7 »

M Gschwind, ER Altman, S Sathaye, P Ledak, D ... - Computer, 2000 - ieeexplore.ieee.org

... Paul Ledak David Appenzeller **IBM Burlington** ... Dynamic **optimization** can open new **optimization** opportunities ... nine different execution units: two **memory**, four integer ...

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Portable profiling and tracing for parallel, scientific applications using C++ - group of 3 »

S Shende, AD Malony, J Cuny, P Beckman, S Karmesin ... - Proceedings of the SIGMETRICS symposium on Parallel and ..., 1998 - portal.acm.org

... of execution: "multi-threaded, shared **memory**" and "single ... in the presence of **optimization**, and portability. ... to limit traces to specified **profile** groups. ...

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Continuous program optimization: Design and evaluation - group of 8 »

T Kistler, M Franz - Computers, IEEE Transactions on, 2001 - ieeexplore.ieee.org
... into account the costs of **optimization** and **profiling**. ... AND FRANZ: CONTINUOUS PROGRAM

OPTIMIZATION: DESIGN AND ... the data-layout for **memory** intensive programs is ...

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The SimpleScalar tool set, version 2.0 - group of 104 »

D Burger, TM Austin - ACM SIGARCH Computer Architecture News, 1997 - portal.acm.org
... desired compile flags (eg, the correct **optimization** level ... c, =yscall.c, **memory.c**,
regs_c, loader.c, sc ... pester <star> generate a text-based **profile**, as described ...

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A scalable cross-platform infrastructure for application performance tuning using hardware counters - group of 13 »

S Browne, J Dongarra, N Garner, K London, P Mucci - Proceedings of Supercomputing, November, 2000 - doi.ieeecomputersociety.org
... [4] David Cortesi, Origin 2000 and Onyx2 Performance Tuning and **Optimization** Guide. ... IBM RS/6000 Division, May 1999. ... **Memory** hierarchy access events ...

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Method for estimating statistics of properties of memory system transactions - group of 3 »

JA Dean, JE Hicks Jr, CA Waldspurger, WE Weihi - US Patent 6,332,178, 2001 - Google Patents
... to Support **Profile-Driven Optimization**, Proceedings of the ... IBM Technical Disclosure Bulletin, "Automatic Program ... related to specific **memory** transactions, such ...

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Apparatus and method for monitoring a computer system to guide optimization - group of 2 »

JA Dean, JE Hicks Jr, GZ Chrysos, CA Waldspurger, ... - US Patent 6,374,367, 2002 - Google Patents
... Data References in Unified Cache", IBM Technical Disclosure ... such as instructions, or **memory** references, or ... state information is analyzed to guide **optimization**. ...

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Profile-directed restructuring of operating system code - group of 6 »

WJ Schmidt, RR Roediger, CS Mestad, B Mendelson, I ... - IBM Systems Journal, 1998 - research.ibm.com
... on other platforms within IBM, we began ... better advantage of the **memory** paging system ...

process: instrumentation, benchmarking, and feedback-directed **optimization**. ...

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... to Support **Profile-Driven Optimization**, Proceedings of the ... IBM Technical Disclosure Bulletin, "Technique for Specu ... related to specific **memory** transactions, such ...

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Method for scheduling contexts based on statistics of memory system interactions in a computer ... - group of 2 »

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 Bulletin,
 "Processor Performance ... related to specific **memory** transactions, such ...
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**Performance Analysis Using the MIPS R10000 Performance Counters -
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M Zagha, B Larson, S Turner, M Itzkowitz - Supercomputing, 1996. Proceedings of the 1996
 ACM/IEEE ..., 1996 - [ieeexplore.ieee.org](#)
 ... such as the Intel Pentium [16], **IBM** Power2 [14 ... to enable common subexpression
 elimination of **memory** references ... improvements from loop nest **optimization** and inter ...
[Cited by 162](#) - [Related Articles](#) - [Web Search](#)

Examination of a **memory access classification scheme for pointer-intensive
 and numeric programs - group of 7 »**

L Harrison - Proceedings of the 10th international conference on ..., 1996 - [portal.acm.org](#)
 ... Wood used their CProf cache **profiling** system to ... programs are compiled with standard
optimization, and the ... generates the contents of all **memory** locations that ...
[Cited by 50](#) - [Related Articles](#) - [Web Search](#)

Profile-guided post-link stride prefetching - group of 3 »

CK Luk, R Muth, H Patil, R Weiss, PG Lowney, R ... - Proceedings of the 16th international
 conference on ..., 2002 - [portal.acm.org](#)
 ... compilers (eg, the **IBM** 5) and HP 30 ... of allocation for other objects, the **memory** lay-
 out in ... also illustrates the following two opportunities for **optimization**. ...
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S Chatterjee, CD Brooks - Multimedia and Expo, 2002. ICME'02. Proceedings. 2002 IEEE ...,
 2002 - [ieeexplore.ieee.org](#)
 ... Christopher D. Brooks **IBM** Software Group 3901 S ... Version 4.2, using the **optimization**
 flag -fast ... obtained instruction counts and **memory statistics** through execution ...
[Cited by 12](#) - [Related Articles](#) - [Web Search](#)

**Contrasting characteristics and cache performance of technical and multi-
 user commercial workloads**

AMG Maynard, CM Donnelly, BR Olszewski - ACM SIGPLAN Notices, 1994 - [portal.acm.org](#)
 ... was not traced if the software **profile** of an ... applications that would have larger **memory**
 requirements (and ... The traces were collected on several **IBM** RISC System ...
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The Influence of Caches on the Performance of Sorting - group of 21 » •

A LaMarca, RE Ladner - Journal of Algorithms, 1999 - Elsevier
 ... well as to keep long term **statistics** on cache ... Thus, the number of **cache misses** per
 key in ... Multiquicksort employs a second **memory optimization** in similar spirit ...
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**SPaid: software prefetching in pointer-and call-intensive environments -
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 ... methods to improve paging behavior of main **memory** [HG71, Fer74 ... completion and
 were
 compiled with the **IBM** XL family ... and XIC for C) at full **optimization** under AIX ...

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Profiling I/O interrupts in modern architectures - group of 8 »

L Schaelicke, A Davis, SA McKee - Modeling, Analysis and Simulation of Computer and ..., 2000 - [ieeexplore.ieee.org](#)

Profiling YO Interrupts in Modern Architectures ... Intel Pentium-Pro [2], MIPS R10000/R12000 [131, **IBM** Power2 [22 ... a large file (equal to the main **memory** size of ...

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SPLASH: Stanford parallel applications for shared-memory - group of 7 »

JP Singh, WD Weber, A Gupta - ACM SIGARCH Computer Architecture News, 1992 - [portal.acm.org](#)

... Systems 1.31) compilers, using the -O2 level of **optimization**. ... altering the interactions with the **memory** system on ... program structure and **profiling** information. ...

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Integrating Fine-Grained Message Passing in Cache Coherent Shared Memory Multiprocessors - group of 11 »

DK Poulsen, PC Yew - Journal of Parallel and Distributed Computing, 1996 - [citeseer.csail.mit.edu](#)

... the National Security Agency, and an **IBM** Resident Study ... This type of **optimization** can be especially useful ... **memory** modules, are full-mapped, and are organized ...

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A performance methodology for commercial servers - group of 5 »

SR Kunkel, RJ Eickemeyer, MH Lipasti, TJ Mullins, ... - **IBM** Journal of Research and Development, 2000 - [research.ibm.com](#)

... or cycle time), and for software **optimization**, including compiler ... that access the system's main **memory** at high ... **IBM** servers address these requirements using the ...

[Cited by 18](#) - [Related Articles](#) - [Cached](#) - [Web Search](#) - [BL Direct](#)

Understanding why correlation profiling improves the predictability of data cache misses in ... - group of 5 »

TC Mowry, CK Luk - Computers, IEEE Transactions on, 2000 - [ieeexplore.ieee.org](#)

... numbered nodes are adjacent in **memory**.) (a) Example ... each application with -O2 **optimization** using the ... LUK: UNDERSTANDING WHY CORRELATION PROFILING IMPROVES THE ...

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TL Johnson, DA Connors, MC Merten, WW Hwu - IEEE Transactions on Computers, 1999 - [doi.ieeecs.org](#)

... The **profiling** results for a 100,000-cycle sample of ... implementation of the cache bypassing **optimization** was presented ... choices are made by a **Memory** Address Table ...

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A Portable Programming Interface for Performance Evaluation on Modern Processors - group of 16 »

S Browne, J Dongarra, N Garner, G Ho, P Mucci - International Journal of High Performance Computing ..., 2000 - [hpc.sagepub.com](#)

... being developed at the **IBM** Advanced Computing ... parallelization, OpenMP) and distributed-memory (MPI, HPF ... **profiling** analysis, and relating **profiling** results to ...

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Unified compilation techniques for shared and distributed address space machines - group of 3 »

CW Tseng, JM Anderson, SP Amarasinghe, MS Lam - Proceedings of the 9th international conference on ..., 1995 - portal.acm.org

... the Intel Paragon, Thinking Machines CM-5, and **IBM SP-2** ... **Cache Misses** There are two

kinds of **memory** subsystem **optimization** techniques: minimizing **cache** ...

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E Altman, M Gschwind, S Sathaye... - Research Report RC21665, **IBM TJ Watson Research Center**, ..., 2000 - research.ibm.com

... is an implementation of the **IBM POWER** processor ... Load/store telescoping eliminates dependencies through **memory**. ... **optimization** opens new **optimization** opportunities ...

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Whole-program **optimization** for time and space efficient threads - group of 5

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D Grunwald, R Neves - Proceedings of the seventh international conference on ..., 1996 - portal.acm.org

... **ibm**. ... We think it's still likely that the context switch **optimization** will be ... Although dynamic **memory** allocation is usually considered to be significantly more ...

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Method for estimating **statistics** of properties of interactions processed by a processor pipeline - group of 3 »

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... of Program Counter Sampling, **IBM Technical Disclosure** ... to Support **Profile-Driven Optimization**, Proceedings of the ... et al., Informing **Memory** Operations: Providing ...

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[book] Performance Debugging and Tuning Using an Instruction-set Simulator - group of 8 »

PS Magnusson, J Montelius - 1997 - citeseer.csail.mit.edu

... examples. 3.1 **Profiling** A profiler gathers and presents **statistics** that are related to a **memory** address range. A ...

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Method for providing virtual **memory** to physical **memory** page mapping in a computer operating system ... - group of 3 »

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... 79-81. **IBM Technical Disclosure Bulletin**, "Instruction Match Function for Processor Performance Monitoring", Dec. ... 710 \SAMPLE **MEMORY** INTERACTION ... **PROFILE RECORD** ...

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Dynamic binary translation and **optimization** - group of 10 »

K Ebcioğlu, E Altman, M Gschwind, S Sathaye - Computers, IEEE Transactions on, 2001 - ieeexplore.ieee.org

... with reasonable translation overhead and **memory** usage. ... compilation, binary translation,

dynamic **optimization**, just-in ... The authors are with the **IBM TJ.Watson** ...

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Profile-Based Energy Reduction in High-Performance Processors - group of 16 »

M Huang, J Renau, J Torrellas - ... ACM Workshop on Feedback-Directed and Dynamic Optimization, 2001 - ece.rochester.edu

... The architecture loosely models an **IBM** Power3 chip ... cache [18] in the instruction **memory** hierarchy. ... the IRIX MIPSPro compiler version 7.3 with -O2 **optimization**. ...

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Increasing the accuracy of statistical simulation for modeling superscalar processors - group of 2 »

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... the synthetic trace matches the statistical **profile** of the ... D-cache access time or a main **memory** access time ... compiler version 5.6 with the **optimization** flag set ...

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... supported in part by an **IBM** Partnership award ... leading references can overestimate **memory** parallelism, since ... be measured through cache simulation or **profiling**. ...

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Binary translation and architecture convergence issues for IBM system/390 - group of 4 »

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... point unit, since IEEE floating point subsumes the capabilities of **IBM** floating point ... A special **optimization** is applicable to short **memory-to-memory** ...

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WHY ILP - IEEE Micro, 1996 - doi.ieeecomputersociety.org

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G Teixeira - 2002 - gec.di.uminho.pt

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E Nahum, D Yates, J Kurose, D Towsley - Proceedings of the 1997 ACM SIGMETRICS international ..., 1997 - portal.acm.org

... suremcntGroupFellowship and is currently with the **IBM** TJ Watson ... network protocol processing,

deriving **statistics** such as ... focusing solely on the **memory** behavior of ...

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... by a Faculty Development Award from **IBM**. ... Programming Languages]: Processors—compilers;

optimization General Terms ... on a detailed **memory profile** captured from ...

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HPCVIEW: A Tool for Top-down Analysis of Node Performance - group of 7 »

J Mellor-Crummey, RJ Fowler, G Marin, N Tallent - The Journal of Supercomputing, 2001 - Springer

... of floating point operations to **memory** references within ... line instance information with raw **profile** data will ... the code generated at each level of **optimization**. ...

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LimitLESS directories: A scalable cache coherence scheme - group of 21 »

D Chaiken, J Kubiawicz, A Agarwal - Proceedings of the fourth international conference on ..., 1991 - [portal.acm.org](#)

... ducing network traffic and average **memory** access latency. ... the performance of a full-map directory with the **memory** overhead of a limited directory. ...

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Evaluating non-deterministic multi-threaded commercial workloads - group of 8 »

AR Alameldeen, CJ Mauer, M Xu, PJ Harper, MM ... - Proceedings of the Fifth Workshop on Computer Architecture ..., 2002 - [cae.wisc.edu](#)

... Sun's WorkShop C 6.1 with aggressive **optimization**. ... by introducing minor perturbations in **memory** system latencies ... and CCR-0105721, an **IBM** Graduate Fellowship ...

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K London, S Moore, P Mucci, K Seymour, R Luczak - Department of Defense Users' Group Conference Proceedings, ..., 2001 - [cs.utk.edu](#)

... is planned on the **IBM** Power3 SMP. ... The **memory** utilization extension is expected to be ... Infrastructure for Application Performance **Optimization** Using Hardware ...

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Analyzing and tuning memory performance in sequential and parallel programs - group of 4 »

MR Martonosi - 1993 - [reports.stanford.edu](#)

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... of object code size on instruction **memory** performance ... control flow graphs, and the **profile** information are ... cc version 2.0.0 and the **optimization** level specified ...

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M Arnold, M Hind, BG Ryder - Proceedings of the 17th ACM SIGPLAN conference on Object-..., 2002 - portal.acm.org

... To keep overhead low, the IBM DK 1.3.0 [43, 44 ... For example, recording all **memory** refer- ences [19] or ... Exception Directed **Optimization** (EDO) [38] also adds in ...

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Scalable Analysis Techniques for Microprocessor Performance Counter Metrics - group of 18 »

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... for refining their code **optimization** efforts. ... main **memory** for a total of 64 GB system **memory**. A Colony SPSwitch--a proprietary **IBM** interconnect-- connects the ...

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ONLINE PROFILING AND FEEDBACK-DIRECTED OPTIMIZATION OF JAVA - group of 8 »

M ARNOLD - 2002 - cs.rutgers.edu

... 94 IBM DK 1.3.0 94 Hotspot Performing **profiling** and **optimization** online, during ...

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... (TI, IBM, AT&T) Of ... Maximum **optimization** was set using the +03 option. ... code so that

ex- ternal library functions are called for every **memory** access instruction. ...

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System-level power optimization: techniques and tools - group of 17 »

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... If decompression had required off-chip **memory** access, MPEG ... From **profiling**, the main

computational kernels are extracted ... System-Level Power **Optimization** • 127 ...

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Modeling and Analyzing CPU Power and Performance: Metrics, Methods, and Abstractions - group of 2 »

M Martonosi, D Brooks, P Bose - SIGMETRICS 2001/Performance 2001-Tutorials, 2001 - ee.princeton.edu

... **optimization** □ Trade off parallelism against clock frequency ... Princeton Wattch □

IBM PowerTimer ... Table-lookup based power models for **memory** and ...

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Better Global Scheduling Using Path Profiles - group of 12 »

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... the program and thus can hurt **memory** system performance. ... a Pettis and Hansen-style [15] procedure- placement **optimization**. ... 3.1 Efficient general path **profiling** ...

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... all **memory** refer- ences made by **IBM 360** programs ... with the highest level of **optimization**,

-04 and ... on address arithmetic and overhead **memory** references performed ...

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... Table 1: Program **statistics** for the baseline architecture ... 16-way dynam- ically scheduled

microprocessor with two levels of instruc- tion and data cache **memory**. ...

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M Ashouei, D Jiang, W Meleis, D Kaeli, M El- ... - International Journal of Systems, Science and Technology, 2002 - [ducati.doc.ntu.ac.uk](#)

... is achieved on the 4-processor 667MHz Alpha shared-**memory** system ... **2 PROFILE-GUIDED**

OPTIMIZATION Profiling has been used to guide a number compilation and program ...

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The Importance of Prepass Code Scheduling for Superscalar and Superpipelined Processors - group of 21 »

DM Lavery, PP Chang, SA Mahlke, WY Chen, WW Hwu - IEEE Transactions on Computers, 1995 - [doi.ieeecomputersociety.org](#)

... US Department of Energy and the **IBM** Corporation. ... **memory** access sequence control for

memory dependences ... general percolation rather than compiler **optimization** bugs ...

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... In conjunc- tion with the global **optimization** ability of our ... Subsystem The speed of the CPU/**memory** interconnection in ... modern computer system, from the **IBM PC** to ...

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... a new paradigm that utilizes extensive **profiling** and powerful off ... Research on **memory** hierarchy **optimization** can be clas ... The gap in **memory** and processor speed is ...

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An overview of common benchmarks - group of 7 »

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... the Fortran compiler's code **optimization** quality: For ... Size, procedure **profile**, and

language- feature distribution. ... parameters are allocated in **memory** in the ...
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Wisconsin Architectural Research Tool Set

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 ... function entry and exit, and **memory** allocation and ... and James R. Larus, "Optimally **Profiling** and Tracing ... 2. Execution Time Speedup at **Optimization Level -03** ...
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PPM TRANCOSO - 1998 - historical.ncstrl.org
 ... systems include Oracle 8 [13], **IBM's** DB2 [14] ... and those that do neglect the cache **memory**. ... Adapt existing blocking algorithm **optimization** for pipelined database ...
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2. **[Redbook - Understanding Performance Tuning Theory for IBM OS/2 LAN Server](#)** 1994-11-06
This document provides the performance information for LAN Server Version 3.0 and 4.0 products. This covers the detail description of LAN Server's mechanism including SMB protocols and buffering logics.

3. **Redbook - IBM PC Server Disk Subsystem Configuration and Sizing** 1995-11-07
This document provides detailed information on how to select the right server system for your business needs and how to optimal configure the disk subsystem in IBM PC Server products. The document provides example configurations as well as reference material on the IBM PC Server disk subsystem.
4. **Redbook - OS/390 Release 2 Implementation MVS, SMP/E, SDSF, and RMF** 1997-01-22
This redbook describes the new functions in OS/390 Release 2. It was written for systems programmers who need to understand these functions and the considerations for implementing them. Several practical examples are presented to demonstrate the use of these new functions. Some knowledge of OS/390 and MVS/ESA is assumed. The OS/390 Release 2 ServerPac installation process is described by showing the process steps used in the installation of the ITSO OS/390 Release 2 system. An enhancement for up to 10 additional data sets may be concatenated to SYS1.PARMLIB at IPL. An installation may, if desired, dynamically change the logical parmlib by switching to another set of parmlib data sets through use of a new operator command. New enhancements have been made in LNKST and LPALST. In addition, a parmlib symbolic pre-processor tool, which is an ISPF-based interactive dialog, is available to show you what your system symbolics and parmlib members would be, given a hypothetical hardware and software configuration. A new enhancement to Global Resource Serialization uses a star topology as an alternative to the current ring implementation. A check list is provided that makes an inventory of the analysis and activities to be followed while planning a migration to a GRS star configuration. An appendix describes a tool that monitors supervisor calls 56 and 48 (ENQ/RESERVE/DEQ) and collects data about the resources serialized and the requesters. Also, an appendix describes a sample exit ISGGREX0 based on the ISGGREXS
5. **Redbook - IBM 2210 Nways Multiprotocol Router Description and Configuration Scenarios - Volume 1** 1997-06-20
This redbook describes the IBM 2210 Nways Multiprotocol Router and its operating software, the Nways Multiprotocol Routing Services (MRS). It provides a technical overview of functions implemented by the IBM 2210 such as bridging, multiprotocol routing, data link switching, frame-relay boundary access node and LAN Network Manager support. One chapter is dedicated to the important special features implemented by the IBM 2210, such as WAN Restoral, WAN Reroute, Dial-on-Demand, Bandwidth Reservation, and EasyStart. The redbook also contains practical scenario descriptions and implementation, showing details of the topology and the commands entered in the console of the IBM 2210. This redbook will help you build a routing environment using the IBM 2210 Nways Multiprotocol Router. With detailed step-by-step implementation of various scenarios, this redbook can be used as a sample when configuring not only the basic routing but also all the additional special features available in this product. Some knowledge of networking architectures and protocols is assumed.
6. **Redbook - OS/390 MVS Parallel Sysplex Capacity Planning** 1998-01-08
Capacity planning in the Parallel Sysplex environment must address specific transition and growth considerations. Opportunities exist for balancing your workload across systems in a Parallel Sysplex. Resources such as systems, Coupling Facilities and links belonging to a Parallel Sysplex have a finite capacity which, when exhausted, may slow down or disrupt service to users. You can avoid these conditions and significantly increase the productivity of

users by careful capacity planning. This redbook provides an overview of capacity planning. It defines the process and the vocabulary, and provides detailed explanations about how to do capacity planning in the Parallel Sysplex environment. Related IBM tools that help in this process are also discussed. Capacity planning considerations for OS/390 V2R4 and related subsystems such as the DB2, IMS/DB and CICS/VSAM RLS data sharing environments are included. For the DB2 environment, a methodology is outlined for estimating the CF locking rates and for obtaining locking information based on DB2PM reports from your current system. A detailed description of RMF reporting for a Parallel Sysplex is provided. Case studies are included for Quick-Sizer modelling of DB2 and IMS/DB data sharing. These case studies are constructed so that you can try them while reading the redbook. Additionally, information and case studies are included for UNIX capacity planning in a S/390 environment. A discussion and case study are included for a CF processing model, which enables you to e

7. **Redbook - AS/400 Communication Performance Investigation - V3R6/V3R7** 1998-01-13

Improving communication performance is not a trivial task. The purpose of this redbook is to discuss how to manage communications performance and ways to locate the problem areas in communication performance. This redbook collects a large amount of the performance information from several sources and presents it in an ordered manner. The databases created by the Performance Tools/400 were used to give the key performance indicators. This redbook is intended for technical professionals including network designers who want to tune the IBM AS/400 system to improve communications performance. An intermediate knowledge of the Performance Tools/400 (5716-PT1) and Query/400 (5716-QU1) is assumed.

8. **Redbook - Consolidating UNIX Systems onto OS/390** 1998-06-04

This redbook describes the IBM S/390 Enterprise Server as a target for the consolidation of applications currently running on multiple distributed UNIX servers. The objectives of the book are: to show how server consolidation is economically justified in many situations, to position S/390 within the spectrum of consolidation offerings, to show that S/390 should be the platform of choice for selected environments and, to provide some practical guidance in the planning and execution of a project to consolidate multiple UNIX servers onto OS/390.

9. **Redbook - IBM Versatile Storage Server** 1998-08-21

This redbook gives a broad understanding of the new architecture of the Versatile Storage Server (VSS). This book provides an introduction, and describes in detail the architecture, technology, data flow, configuration, migration and recovery aspects for the VSS.

10. **Redbook - RS/6000 Scientific and Technical Computing: POWER3 Introduction and Tuning Guide** 1998-09-18

This redbook provides information to help you understand and exploit the new generation of computer systems based on the RS/6000 POWER3 architecture. Specifically, this publication will address the following issues: POWER3 features and capabilities CPU and **memory optimization** techniques, especially for Fortran programming AIX XL Fortran Version 5.1.1 compiler capabilities and which options to use Parallel processing techniques and performance Available libraries and programming interfaces Performance examples on commonly used kernels and on several full applications The anticipated audience for this redbook is as follows: Application developers End users who